

jc781 U.S. PTO

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09/994508

11/27/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

MT	O.I.P.E.	PATENT DATE
SCANNED	NG 2	Q.A. 30

APPLICATION NO. 09/994508	CONT/PRIOR F	CLASS 438	SUBCLASS 786 ²	ART UNIT 28124	EXAMINER S. RAO
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APPLICANTS

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TITLE

Method of fabricating semiconductor devices having low dielectric interlayer insulation layer

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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